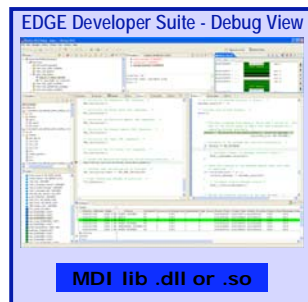


EDGE MAJIC JTAG Probe

The EDGE MAJIC JTAG Interface Controller provides a robust target connection, supports multi-core debug and ships out-of-the-box supporting all processors, debugger APIs and partner reference designs (see [Supported Processor](#) list), including pre-configured flash utilities, target initialization files and MAJIC Startup files.

The EDGE MAJIC architecture is designed to simplify target board bring-up and support by independent layering of debugger software interface APIs, internal firmware tailored to individual cores, and pre-configured target support files for each reference design, independent from the tool choices higher up the chain.

Several standard JTAG cables complete the target connection, so one probe can easily support multiple processors, architectures and software environments.



BENEFITS:

✓ Multiple Debugger API Support

EDGE MAJIC probes include multiple shared libraries and executables, support industry standard debugger APIs enabling one JTAG Probe to support multiple software environments.

✓ Setup Wizard, Templates & Target Initialization files

With EDGE MAJIC probes, you get out-of-the-box setup for reference designs and ease of transition to OEM board designs.

✓ Comprehensive cache and MMU/MPU support

This is defined for each core type, including all write-through and/or write-back cache mode options, memory management or protection modes, and DMA, where available.

✓ Pre-Configured Target and Sample Files

Builds early confidence in processor, memory system and target controls, and provide templates for transition to OEM targets

✓ Multiple price point bundles

- **MAJIC-LT**- Supports EDGE Developer Suite*, EDB* and GDB only, 10 MHz JTAG clock and 3.3V targets.
- **MAJIC-LX** – Adds programmable JTAG clock 1 to 40 MHz, Auto-Power-Sense 1.2V to 3.3V, and includes all OpenDebug API support.
- **MAJIC-MT** – All MAJIC-LX features, plus multicore debug sessions and Intel XScale trace upload

MAJIC Solutions:

Layered Target Interface Software:

- Multiple debugger APIs
- Multiple CPU Architectures supported
- MAJIC firmware support for each core type
- Target board and core base start-up files
- Target support initialization/control files
- Standard memory test pattern options

MAJIC Probe Features

- Non-intrusive JTAG connection
- Non-intrusive connect mode
- Interactive debug mode
- Hardware breakpoint support
- Unlimited software breakpoints
- Programmable JTAG clock
- Download speeds to >400KB/s
- Supports RT Adaptive clock mode for ARM
- Preconfigured and customizable template startup files
- Comprehensive memory tests supports
- NOR Flash programming utilities
- Scripts control target-specific functions
- HW and SW QuickStart guide
- Ethernet and serial cables
- Power supply brick with international power cord
- MONICE command line debugger
- MONICE scripting language (for watchdog timers, on-stop, at-stop or on-go controls)
- NOR Flash programming utilities
- Standard JTAG cables

MAJIC Standard JTAG Cables:

ARM		MIPS	
CA-ARM14	14-pin	CA -EJ12	12-pin
CA -ARM20	20-pin	CA -EJ20	20-pin
CA -FPJ12	12-pin small	CA -EJ52	52-pin
CA -TX14	TI 14-pin	CA -J10	10-pin
		CA -J12	12-pin
		CA -MIPS14	14-pin

✓ Processors

ARM and MIPS standard core and architectural licensee core support – MAJIC probes support 30 standard cores, 46 cores from architectural licensees and 207 reference board designs, with straight-forward adaptation to new SOC designs based on those cores.

MAJIC SOLUTIONS FOR:

ARM® Core/Processor Support

ARM cores	7TDMI, 7TDMI-S, 7EJ-S, 720T, 9TDMI, 9E-S, 920T, 922T, 926EJ-S, 940T, 946E-S, 966E-S, 968E-1136JF-S, 1156T2, 1176JZF-S, MPcore
Altera	EXPA Series
Atmel	AT7x, AT91x
Cirrus	EP7x, EP9x
Conexant	CN/CXxxxxx
Faraday	FA526 core
Freescale	i.MX family
GlobespanVirata	Helium 100, 210-80, 500
Marvell	Feroceon, PXA3xx
NetSilicon	NET+ARM, NS7520, NS9360, NS9750
Oki Semi	ML67xxx
Qualcomm	MSMxxxx
Samsung	S3xxxxx
Sharp	LH7xxxx
ST Micro.	ARM7/9 SOCs, Nomadik
Texas Inst.	ARM7 in Telogy DSPs ARM7/9 in OMAP family
Winbond	W90N740

Intel XScale® Processor Support

Intel® Storage Components	IOP310 80200 80219
	IOP321 80321
	IOP33x 8033x

Intel® Control Plane Processor

IXC1100

Intel® eXchange Architecture

IXP42x
IXP46x
IXP2350, 2400
IXP2800, 2850

Marvell XScale processors

PXA25x, 26x, 27x and PXA3xx

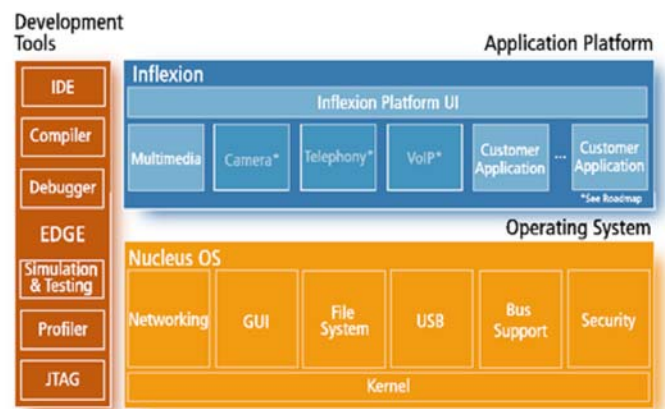
MIPS® Core/Processor Support

MIPS core IP	MIPS32, cnMIPS64 MIPS32, MIPS64 4Kc, m, p 4KEc, m, p 5Kc/f, 24Kc/f, 24KE Architectural licensees
ATI Technologies	Xilleon 22x
Broadcom	BCM11xx, BCM21xx, BCM33xx, BCM43xx, BCM47xx, BCM53xx, BCM58xx, BCM63xx, BCM65xx, BCM70xx, IBCM71xx, BCM73xx, BCM74xx
Cavium Networks	CN2xx Nitrox Soho, CN3xxx OCTEON CN5xxx OCTEON PLUS
Ikanos	AD6xxx
Lexra	LX4/5xxx core IP
Micronas	MDE95xx, VGC
Micronas USA	Cypher 7108, DeCypher 8100
PMC-Sierra	MSPxxxx
Sigma Designs	SMP86xx
Texas Inst.	TNETC/D/V-xxxx

For more information visit us at:

http://www.mentor.com/products/embedded_software/cpu

MAJIC probes have evolved toward a single goal: empowering embedded software/platform developers to bring up new target platforms quickly and easily, with the best technology, the most confidence and the least hassle. Now the EDGE Developer Suite offers a new generation of design and development tools aimed at making embedded developers' lives easier.



Visit our website to learn more about EDGE Developer Suite, Nucleus OS and Inflexion Platform.

www.mentor.com/embedded