



Designer

Author

HDL Designer Series™ v2009.1 Product Feature Matrix

Product Feature			Special Notes
Conversion from RTL to Graphics (Editable)			
HDL File Import	X	X	
Block Diagram	X	X	Creates Editable Graphics that will be used to generate RTL
Interface Based Design (IBD)	X	X	
State Diagram	X	X	
Flow Chart	X	X	
Symbol with Tabular IO	X	X	
Moduleware Libraries	X	X	
External IP Generator Integration	X	X	
Conversion from RTL to Graphics (Editable)			
RTL analysis	X	X	Xilinx , RMM, Altera, Essentials
Code Quality Scoring	X	X	
Visualization / Rendering			
Block Diagram	X	X	
Interface Based Design (IBD)	X	X	
State Diagram	X	X	
Flow Chart	X	X	
Documentation Generation			
OLE	X	X	Windows Only
HTML	X	X	
Printing	X	X	
Graphics Export (SVG/JPG/PNG)	X	X	
Languages and Platforms			
Batch Mode Operation	X	X	
UNIX (32 & 64 bit - Solaris)	X	X	Single executable for 32/64 bit
Windows (2000, XP, Vista)	X	X	
Linux (Redhat/SuSE)	X	X	Redhat Enterprise 4.0/Redhat Enterprise 5.0
VHDL	X	X	
Verilog95, Verilog2001, Verilog 2005	X	X	
SystemVerilog 1800	X	X	Text support, no checks or HDL-to-Graphics
Mixed-HDL	X	X	
C/C++ ModelSupport	X	X	
Design Management			
Multiple Design View Support	X	X	
Data Management	X	X	
Project Manager	X	X	
Non-HDL Data	X	X	Any files such as documents, scripts, etc.
Version Management	X	X	RCS & CVS Included with HDS
Version Management Integration	X	X	Rational Clearcase, Synchronicity DesignSync, Microsoft VSS, ClioSoft SOS, Subversion
Synthesis Tool Integration	X	X	LeonardoSpectrum, PrecisionSynthesis, Design Compiler, Synplify/Synplify-Pro
Digital Simulation Tool Integration	X	X	ModelSim, NC-Sim, VCS/VCSi
FPGA Vendor Flows	X	X	Xilinx ISE, Altera QuartusII
FPSOC Tool Integration	X	X	Xilinx Platform Studio, Altera SOPC Builder
Custom Tools and Flows	X	X	
Editors			
Block Diagram	X	X	
Interface Based Design (IBD)	X	X	
State Diagram	X	X	
Algorithmic State Machine (ASM)	X	X	
Flow Chart	X	X	
Truth Table	X	X	
Symbol with Tabular IO	X	X	
Built-in Text editor (DesignPad)	X	X	
External Text editor integration	X	X	
HDL Templates	X	X	
Simulation Analyzer			
Questa	X	X	Full invocation and debug support
ModelSim PE/SE	X	X	Full invocation and debug support
Cadence NC-Sim	X	X	Full invocation and debug support