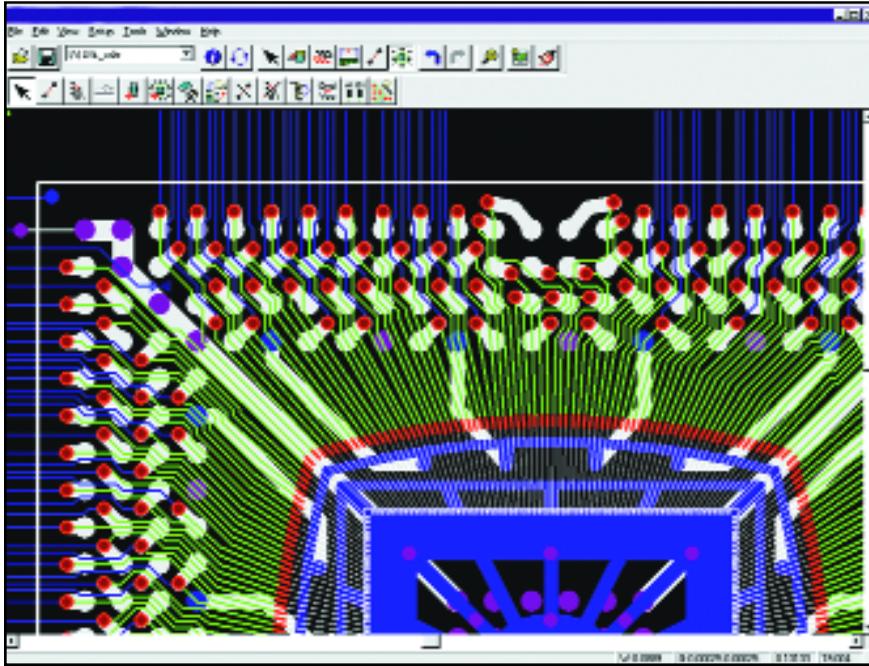


Advanced Packaging Tool Kit



The Advanced Packaging Toolkit reduces design time for PADS® Layout customers who design with bare-die components

Overview

The Chip-on-Board (COB) Toolkit and Advanced Packaging Toolkit can significantly reduce package design time for PADS® Layout users who work with bare-die components such as single- and few-chip modules and chip-on-board.

Both the COB Toolkit and Advanced Packaging Toolkit improve design quality by automating key aspects of the advanced package design process, including die capture, rules-based wire bond design, flip-chip definition, and report generation. The Advanced Packaging Toolkit additionally aids in the routing of single-chip packages and the definition of die flags. A variety of die, die flag, and route wizards and tools speeds design reuse and creation of manufacturing data.

The robust features found in the COB Toolkit and Advanced Packaging Toolkit simplify and support the design of ball grid arrays (BGAs), chip-scale packages (CSPs), multi-chip modules packages (MCM) and COBs.

Major product features

- Cuts design time of substrates with bare-die components
 - Chip-on-Board (COB)
 - Single-chip packages
 - Few- and multi-chip packages
- Automates die creation
- Automatically creates die flags and power rings
- Uses rules-driven placement to automate wire-bond fanout
- Eliminates the complexity and tedium of routing large packages
- Simplifies design process with Die, Die Flag, and Wire-Bond Wizards
- Imports GDSII/ASCII/DXF files

The Die Wizard

A Die Wizard included with the COB Toolkit and Advanced Packaging Toolkit enables construction of die part models from the die description data contained in GDSII or ASCII files.

Direct import of native GDSII/ASCII die files streamlines the design process, simplifies data transfer, and eliminates translation errors. If the GDSII file contains data beyond just the die description, designers can use the wizard's dynamic preview capability and data browsing and filter controls to extract die-specific data easily.

Die parts also can be constructed using data imported from an ASCII text file. The die wizard reads chip-bond pad information such as pad number, location, function, and pad shape and size from a simple comma-delimited text file.

Parameters contained in the wizard's construction templates make it easy to augment and modify data. Corresponding parameters are grouped together and presented in a multi-tab format to provide control over die size, origin offset, pad shape, pad size, pin sequence, and pin function names. When GDSII/ASCII data are unavailable for import, it is easy to use the Die Wizard to craft a die part.

Parametric construction is ideal when early package exploration is necessary, as when the die description is still in flux and exact information is unavailable.

Supplement Missing Data – After importing GDSII/ASCII data into the Die Wizard, parameters in the construction templates can be used to add missing data or to override imported data such as die size or pad shape.

Parametric Construction – The Die Wizard interactively defines die parts using the parameters in the construction templates.

Dynamic Preview Capability – All construction options include dynamic data previewing for immediate graphical feedback during construction.

The Die Flag Wizard

The Die Flag Wizard automatically creates die flags, rings, and their associated solder mask openings. By including solder mask creation, the Die Flag Wizard consolidates design steps and simplifies design creation. Immediate graphical feedback is provided as parameters are entered and modified.

Die Flag Construction – The Die Flag Wizard makes it possible to simplify die-flag construction and reducedesign time by specifying the overall die-flag size relative to the selected die. Choose from predefined outer shapes (rectangle, rounded rectangle, or chamfered rectangle), specify net association, define the number of spokes and orientation, specify paddle coverage, and separate control ring and spoke width during parametric construction.

Automatic Mitering – Acute angles ,mitered automatically during die-flag creation, prevent acid traps and improve manufacturability.

Power Ring Construction – It is possible to create an unlimited number of concentric power rings for an individual die. This is accomplished by specifying ring width and clearance, selecting from predefined ring shapes (rectangle, rounded rectangle, or chamfered rectangle), or specifying an arced ring. This functionality simplifies power ring construction and reduces design time. Users can define and control solder mask openings when constructing die flags and power rings., and specify separate inside/outside compensation values to control solder mask over/undersize.

Defining the solder mask openings, is a simple matter of selecting a die part in the layout editor and using the dialog window. The die flag and rings are constructed automatically on their assigned layers using design copper shapes. Standard editing functions allow shape modification.

Unique Configurations – The die flag wizard is designed to meet most standard design requirements. For designs requiring unique die-flag and ring configurations, a DXF import option is available.

The Wire Bond Wizard

The Wire Bond Wizard provides rules-driven automatic wire-bond fanout capability that enables easy evaluations of routability and substrate bond pad placement trade-offs. The wizard's easy-to-use dialog box provides one-stop definition for all the constraints and parameters necessary for automatic generation of wire-bond fanout patterns. Parameters and constraints are presented in logical groups for simplified setup and iterative exploration.

The Wire Bond Wizard also supports symmetrical and asymmetrical wire-bond fanouts, enabling mixed strategies on the same die. The wire-bond fanout algorithm always attempts to place substrate bond pads in compliance with the specified assembly rules. In the event of insufficient area, substrate bond pads are placed and violations reported, enabling experimentation with alternative strategies while visualizing their results.

Rules-driven Construction – The Wire Bond Wizard automatically generates substrate bond pad placements that comply with wire-bond assembly rules such as wire bond-to-wire bond, wire bond-to-substrate bond pad, substrate bond pad-to-substrate bond pad, minimum- and maximum wire length, and maximum wire-bond angle.

Guide Based Construction and Editing -Predefined ring shapes (rectangle, rounded rectangle, chamfered rectangle, tent, or arced) can be used as construction guides to ensure uniform substrate bond pad placement. During interactive editing, substrate bond pads automatically snap to guides, thus ensuring uniform pad placement.

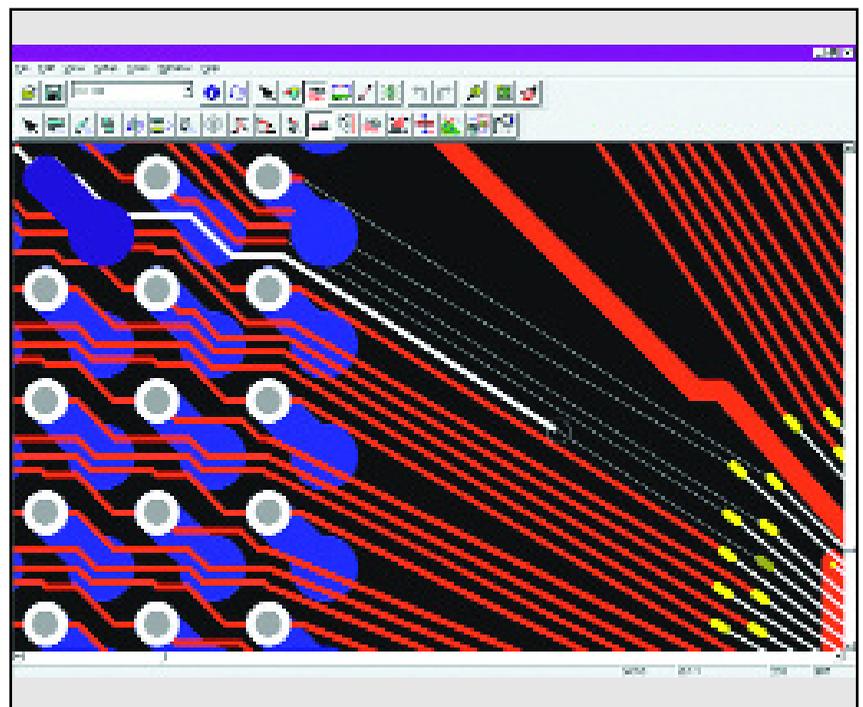
Multiple-Ring – The Wire Bond Wizard supports multi-row and multi-tier wire bond fanouts with unlimited wire-bond guides.

Dynamic Preview Capability – Immediate visual feedback appears as rings are defined and nets assigned.

Save and Reuse Parameters – To minimize setup time, ring definitions and parameters can be saved for reuse in other designs.

The Route Wizard

As die I/O and package pin counts increase, so too does the complexity and tedium of routing large packages. The Advanced Packaging Toolkit's easy-to-use Route Wizard eliminates these problems by combining any-angle routing with pattern recognition to automatically route single-die advanced packages. By optimizing connections based on the configurations of the grid-array fanout and plating tail, the Route Wizard can eliminate manual routing between a die and the package pins. The wizard can also reduce design time by providing interactive autorouting of die quadrants and sides for use in packages with multiple dies.



Interactive Route Editor supports any-angle routing to substrate pads, with or without a netlist

Automatic Generation of Package

Connections – The Route Wizard automatically synthesizes connections between a bare die and a specific package layout. It supports any mix of user definitions, thus enabling collaboration between the packaging engineer, the IC engineer, and the board designer to achieve optimized package pin definitions.

Automatic Substrate and Array Pad Fanout

Area array pads, the landing for solder balls on a package, are efficiently fanned out in regular patterns to conserve space, ease routing congestion, and support manufacturability.

Automated Any-angle BGA Package Routing

Because traditional auto-routers are not optimized to handle substrate bond pads'any-angle rotation, they often require special methodologies to escape the die and weave through the array pattern to the destination pad. The Advanced Packaging Toolkit's route wizard, together with PADS Router, (interactive route editor), can automate this task. Together they offer automatic routing functions that complete any angle interconnects for wire-bonded dies within BGA/CSP packages. By following a user-specified methodology, the Advanced Packaging Toolkit provides high completion rates while minimizing design time and effort.

Automated Plating Tail Generation -

Packages that require electroplating during fabrication must have each connection routed to an external plating bar. As package density increases, the plating tails require a design effort equal to the base interconnect. If plating tails are not included during initial feasibility evaluation, there is a chance of selecting an incorrect package, which can cause in product delays. The Route Wizard optimizes the package interconnects by automatically routing plating tail interconnects from the package feature closest to the edge of the package.

	<i>COB Toolkit</i>	<i>Advanced Packaging Toolkit</i>
Die Wizard	X	X
Wire Bond Wizard	X	X
Report Generation	X	X
Die Flag Wizard		X
Route Wizard		X

The COB Toolkit and Advanced Packaging Toolkits offer scalable functionality to address your bare die design needs

Package Feasibility Evaluation – As package pin-counts and design volumes increase, packaging engineers must evaluate design feasibility and commit to a package almost immediately.

Unfortunately, traditional feasibility test methods can lead to incorrect package choices, forcing a redesign late in the project, often resulting in a loss of business. The Route Wizard enables a quick determination of package feasibility, based on the die and a set of technology rules. Once feasibility is established, automatic routing completes and optimizes the design for manufacturing needs. This saves time in planning and manually routing. It also reduces design times significantly through interactive auto-routing of die quadrants and sides for use in packages with multiple die.

Report Generation

Once the package design is complete, it must be documented. This can be extremely tedious and error-prone, as information must be correlated from multiple sources. The COB Toolkit and Advanced Packaging Toolkit automates this last step in the design process by automatically generating table-formatted documentation:

- Automatically assigns the ball alphanumeric reference at its corresponding substrate bond finger
- Automatically generates technical definitions based on intelligent design and library data
- Creates intelligent connectivity data between the die, wire bonds, substrate route, and package output pins

Summary

The COB Toolkit and Advanced Packaging Toolkit automates all aspects of the advanced package design process.

From feasibility studies through final report generation, the COB Toolkit and Advanced Packaging Toolkit offers higher completion rates and reduced design times to any PADS-Layout designer working with bare-die components.

Platform and Operating Systems

Operating Systems

- Windows XP (service pack 2)
- Vista on Intel-based systems

Memory Requirements

- Windows Vista Ultimate or Business Editions:
 - 2 GB or more
- Windows XP Professional (SP2): 1 GB or more

PC Hardware

- Pentium IV 2+ GHz recommended.
- High-speed CPU recommended.
- Three-button mouse or mouse with scroll wheel recommended.

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