



NEWS FROM TEMENTO SYSTEMS (France)

Temento Systems Announces Tight Integration of DiaLite 4.5 Edition with MENTOR GRAPHICS PRECISION Synthesis

Montbonnot, France, May 8, 2006 — TEMENTO SYSTEMS, a leading provider of software and hardware solutions for the debug and the verification of FPGAs and SoCs, today announced the support of Mentor Graphics' Precision® Synthesis tool into the latest version of DiaLite Instrumentation, release 4.5.1.

The support of Precision Synthesis allows designers and users to utilize the DiaLite Instrumentation tool to insert IPs instrumentation cores and to synthesize their designs in a fast and simple way.

"We welcome Temento's support of Precision Synthesis, as it enables our customers to effectively debug FPGA designs on the board," stated Daniel Platzker, Product Line Director, FPGA Synthesis of the Design Creation and Synthesis Division at Mentor Graphics. "We provide the most comprehensive vendor-independent FPGA design flow from design entry to simulation to synthesis to integration with PCB design. Temento's support complements Mentor's complete solution for ASIC prototyping and complex FPGA design."

"Mentor Graphics' Precision Synthesis customers and FPGA/SOC developers will take benefits of this expanded DIALITE support of Precision Synthesis" said Yves Devigne, Director, Business Development at Temento Systems. "By delivering TEMENTO SYSTEMS best-in-class debug tools with the automated generation of Precision Synthesis projects, we will provide Mentor Graphics' users with the most achieved debug solution on the market."

"In a general way, we are now working closely with Mentor Graphics' Precision Synthesis team, optimizing our interfaces to capitalize on the other features of DiaLite in terms of System level design and assertion based verification as well as other Temento Systems hardware, including high performance JTAG controllers and external Gigabits storage capabilities. We believe that through this close partnership, we expect to offer Mentor Graphics' customers the most advanced design and debug solution available on the market."

About DiaLite Instrumentation

DiaLite Instrumentation debug and verification tools offers best-in-class editions for "on chip" signal analysis and data collection "on silicon" to find behavioral malfunctions, plus the capability to visualize the HDL code executed when such an error occurs and finally check the most important specifications of the entire system using the latest Assertion Based Verification techniques.

DiaLite is the first tool ever supporting designs in VHDL and Verilog and mix of both, while offering the same debug capabilities on all major FPGA devices and on single or multiple FPGA designs.

Availability

This new software release is available from TEMENTO SYSTEMS through its worldwide sales channels as stated in the following web page:

<http://www.temento.com/sales/vendor.php>

About Temento Systems

Temento Systems S.A. provides Electronic Design and Test Automation (EDTA) solutions that enable engineers to test and debug electronic products, including System on Chip (SoC), FPGAs, Boards, Multi-Chips Modules (MCMs) and Systems. Unlike traditional EDA software providers, Temento Systems offers a broad range of solutions focused on systems design test, starting from the earliest stage of design definition (virtual test), straight through hardware testing (physical test).

<http://www.temento.com>

About Mentor Graphics

Mentor Graphics Corporation (Nasdaq: MENT) is a world leader in electronic hardware and software design solutions, providing products, consulting services and award-winning support for the world's most successful electronics and semiconductor companies. Established in 1981, the company reported revenues over the last 12 months of over \$700 million and employs approximately 4,000 people worldwide. Corporate headquarters are located at 8005 S.W. Boeckman Road, Wilsonville, Oregon 97070-7777. World Wide Web site:

<http://www.mentor.com/>.



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